

知 某局点WX6108E日志报错问题

其他硬件相关 张文宁 2023-06-29 发表

组网及说明

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## 问题描述

设备:WX6108E

问题: 板卡都是normal, 但是一直在系统报错:

```
SZ-CX-AC-42_MAN_WX6108E>May 11 17:04:37:707 2023 SZ-CX-AC-42_MAN_WX6108E DRVPLAT/4/DrvDebug: -MDC=1; Board fault: chassis 0 slot 1 or chassis 0 slot 3, please check them
May 11 17:04:55:706 2023 SZ-CX-AC-42_MAN_WX6108E DRVPLAT/4/DrvDebug: -MDC=1; Board fault: chassis 0 slot 1 or chassis 0 slot 3, please check them
May 11 17:05:11:707 2023 SZ-CX-AC-42_MAN_WX6108E DRVPLAT/4/DrvDebug: -MDC=1; Board fault: chassis 0 slot 1 or chassis 0 slot 3, please check them
May 11 17:05:31:707 2023 SZ-CX-AC-42_MAN_WX6108E DRVPLAT/4/DrvDebug: -MDC=1; Board fault: chassis 0 slot 1 or chassis 0 slot 3, please check them
May 11 17:05:49:709 2023 SZ-CX-AC-42_MAN_WX6108E DRVPLAT/4/DrvDebug: -MDC=1; Board fault: chassis 0 slot 1 or chassis 0 slot 3, please check them
```

```
May 11 17:06:07:710 2023 SZ-CX-AC-42_MAN_WX6108E DRVPLAT/4/DrvDebug: -MDC=1; Board fault: chassis 0 slot 1 or chassis 0 slot 3, please check them
This dev
Slot No. Brd Type Brd Status Subslot Sft Ver Patch Ver
0 EWPXM3MPUB0 Master 0 WX6100E-7585P05 None
1 EWPXM3MPUB0 Standby 0 WX6100E-7585P05 None
2 EWPXM1MAC0P Normal 0 WX6100E-7585P05 None
3 EWPXM1MAC0P Normal 0 WX6100E-7585P05 None
4 EWPXM2GP44TSSC0 Normal 0 WX6100E-7585P05 None
5 EWPXM2GP44TSSC0 Normal 0 WX6100E-7585P05 None
6 EWPXM1MAC0P Normal 0 WX6100E-7585P05 None
7 NONE Absent 0 NONE None
#SZ-CX-AC-42_MAN_WX6108E>
```

## 过程分析

分析发现slot1和slot3的内敛口没up:

```
====bcm slot 1 chip 0 ps====
ena/ speed/ link auto STP          lrn inter max loop
port link duplex scan neg? state pause discrd ops face frame back
hg0( 2) up 40G FD SW Yes Forward      None F XGMII 16360
hg1( 5) up 21G FD SW No Forward        None F XGMII 16360
hg2( 7) up 21G FD SW No Forward        None F XGMII 16360
hg3( 8) down 21G FD SW No Forward      None F XGMII 16360
hg4(12) down 21G FD SW No Forward      None F XGMII 16360
hg5(14) down 40G FD SW Yes Forward     None F GMII 16360
hg6(16) up 40G FD SW Yes Forward       None F XGMII 16360
hg7(18) up 21G FD SW No Forward        None F XGMII 16360
```

====display devm hgport chassis 0 slot 1====

(slot 1, slot 3):

(unit, port) (unit port)

(0 , 8 ) (0 , 8 )

(0 , 12 ) (0 , 5 )

====bcm slot 1 chip 0 ps====

```
ena/ speed/ link auto STP          lrn inter max loop
port link duplex scan neg? state pause discrd ops face frame back
hg3( 8) down 21G FD SW No Forward      None F XGMII 16360
hg4(12) down 21G FD SW No Forward      None F XGMII 16360
```

随后更换slot3单板后问题解决,替换跟着slot 3单板走.slot3硬件故障.

## 解决方法

slot3硬件故障.返修解决.

