

## 知 H3C万兆核心路由器SR8800如何查看子卡逻辑版本

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可以通过以下命令查看：

```
[H3C-testdiag]display version slot 5 subslot 1
```

```
@
```

```
Subslot 1 info
```

```
Status : Normal
```

```
Type : PIC-GT20R
```

```
PCB : Ver.A
```

```
CPLD : 001
```

```
FPGA : NONE
```

```
MotherCard
```

```
Type : SR01DGT20R
```

```
CoverCard
```

```
Type : SR01DPMB
```

```
PCB : Ver.A
```

```
CPLD : NONE
```

```
FPGA : NONE
```