SIC/DSIC and Link-Aggregation In MSR Series

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Q: Whether SID/DSIC support Link-Aggregation in MSR Series ?

SIC/DSIC module not support Link-Aggregation, the 8 interfaces switch module in $\ensuremath{\mathsf{M}}$ SR2021 not support either. All of this related with the MV chip. MIM/DMIM and FIC/D FIC support Link-Aggregation.