纪合宝 2009-01-14 发表

What's the meaning of TDM bus 2k*2k?

Q: What's the meaning of TDM bug 2k*2k? What's the condition one analog ca rd calling need CPU process?

MSR2040,30 and 50 routers all have TDM bus. TDM bus consists of 32 8Mbps HO b us, and PSTN use 64kbps ADPCM coding/decoding, then bidirectional calling need 1 28kbps, so TDM bus can support 2048(32*8M=2048*128k).

2K*2k means can support 2k tunnel calling. The calling between one analog card inte rfaces like 4FXS will need PU process, and two different card don't need CPU proces