



## What's the meaning of TDM bus 2k\*2k?

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**Q: What's the meaning of TDM bus 2k\*2k? What's the condition one analog card calling need CPU process?**

**A:**

MSR2040,30 and 50 routers all have TDM bus. TDM bus consists of 32 8Mbps HO bus, and PSTN use 64kbps ADPCM coding/decoding, then bidirectional calling need 128kbps, so TDM bus can support 2048( $32*8M=2048*128k$ ).

2K\*2k means can support 2k tunnel calling. The calling between one analog card interfaces like 4FXS will need CPU process, and two different card don't need CPU processes.